## Chapter 20

## ALTERNATING CURRENT

## A.) The Production of AC:

1.) When a coil is placed in a magnetic field and is rotated, a changing flux $\Delta \Phi_{\mathrm{m}}$ through the coil will produce an induced EMF across the coil's leads. A surprising result is found when Lenz's Law is used to determine the direction of the induced current and, hence, the high and low voltage side of the coil as the coil rotates. Follow al ong:
a.) Assume the external magnetic field is directed into the page and the coil is initially facing the B-field (see Figure 20.1a--note that the right side of the coil--the part that will initially rotate into the page-is darker; this has been done to make it easier to follow the coil through the complete $360^{\circ}$ rotation). As the coil begins to rotate, the external flux through the coil's face diminishes (Figure 20.1b). The coil generates an induced current which opposes the decrease of flux, which means an induced B-field is created into the page. The direction of current flow required to do this is clockwise, which means lead A must be the positive side of the coil.
b.) The coil rotates through the no-external-flux position and into the second quarter of its cycle (Figure 20.1c on the next page). In this part of its motion, the external flux is increasing. The induced B-field required to oppose this increase must be out of the page, which requires an induced current that is counterclockwise relative to ourselves. As such, lead A must again


FIGURE 20.1a
flux decreases in first quarter; induced B into page; lead A positive


FIGURE 20.1b be the positive side of the coil.
c.) The coil rotates through the maximum-flux position and into the third quarter of its cycle (Figure 20.1d). In this part of its motion, the external flux is decreasing. The induced B-field required to oppose this decrease must be into the page, which requires a current that is clockwise relative to ourselves. As such, lead A must be negative.


FIGURE 20.1c
d.) Lead A will be negative through the last quarter of the rotation (just as in Part b above, but with the lead positions switched).
e.) Bottom line: The high-voltage side of a rotating coil alternates from one side to the other as the coil turns. This is how alternate current (AC) is generated.
flux decreasing in third quarter; induced $\mathbf{B}$ into page; lead A negative


FIGURE 20.1d
2.) AC Power--From the Power Plant to You:
a.) Power production: Consider a hydroelectric plant. A waterfall turns a turbine, the shaft of which is attached to a giant coil. The coil is suspended in a fixed magnetic field. As the coil rotates, AC is produced across its leads. Sliding contacts tap the alternating voltage.
b.) Transport: Sending the electrical power to the city requires the use of wires. The single biggest source of energy loss during this transfer is due to the heating-up of those wires. As high currents generate lots of heat, the trick is to keep the current as low as possible.

Using a step-up transformer, the voltage is stepped up to, say, 50,000 volts. This drops the current down quite low for the span be tween the power generator and the city. As there are few toasters that can handle 50,000 volts, a step-down transformer steps the voltage at the city down either to 110 or 220 volts AC allowing current-availability to go upward in the process. In that way, power companies can accommodate hundreds of thousands of homes at once.

The entire process is displayed in Figure 20.2 on the next page.
electric power-production


FIGURE 20.2

## B.) The Fundamentals of Alternating Currents:

1.) The circuit symbol for an AC power supply is shown in Figure 20.3.
2.) Figure 20.4 graphs the voltage difference between the two terminals as a function of the time-related variable $\omega$ t. Assuming the voltage difference across the terminals is positive when the left terminal is the higher voltage, we find:
circuit symbol for an AC power supply (note that symbol inside circle is a sinewave)


FIGURE 20.3
a.) At $t=0$ there is no voltage difference between the terminals.
b.) As time progresses the voltage difference between the two terminals gets larger until it hits some maximum (see Figure 20.5a on the next page). The maximum is defined as the amplitude of the voltage function and is sometimes called the

AC voltage


FIGURE 20.4
peak voltage. Under most circumstances this peak voltage is symbolized as $\mathrm{V}_{\mathrm{o}}$.
c.) After reaching its peak, the voltage difference between the terminals decreases back down to zero (see Figure 20.5b). At this point, the high and low voltage terminals reverse. To denote this change, reversepolarity voltages are defined as negative voltages.

Note: The implication is that there is no real difference between a positive or negative voltage when dealing with AC. In one case the high voltage terminal is, maybe, on the left while in the other case the high voltage terminal is on the right. The words "positive" and "negative" are used simply to distinguish between the two situations.
d.) After polarity reversal, the voltage difference between the terminals gets larger with time (Figure 20.5c) until it again reaches its maximum negative peak $\mathrm{V}_{0}\left(-\mathrm{V}_{0}\right.$ on the graph--see Figure 20.5c). It then proceeds back towards zero.


FIGURE 20.5a


FIGURE 20.5b

as time progresses during this period, the vol tage difference across the power supply
terminals reverses and increases
FIGURE 20.5c Once at zero, the polarity changes and the process starts all over again.
e.) The function that defines this behavior is a sine wave. That is, time-dependent voltage sources can be mathematically characterized as:

$$
\begin{aligned}
V(t) & =V_{0} \sin (\omega t) \\
& =V_{0} \sin (2 \pi v t),
\end{aligned}
$$

where $\omega$ is the angular frequency in radians per second of the alternating voltage and $v$ is the more common frequency variable in cycles per second or hertz.

Note: The AC in your home has a frequency of 60 hertz. Multiplying by $2 \pi$ yields a voltage function for home wall-sockets of: $\mathrm{V}(\mathrm{t})=\mathrm{V}_{0} \sin (377 \mathrm{t})$.
3.) The current through a resistor hooked to an AC power supply produces a graph similar to that shown in Figure 20.4. That is, the timedependent current function looks just like the time-dependent voltage function, or:

$$
\begin{aligned}
i(t) & =i_{0} \sin (\omega t) \\
& =i_{0} \sin (2 \pi v t) .
\end{aligned}
$$

Note: For resistors, the frequency of the AC voltage source and the frequency of the current functions will always be the same.

## C.) RMS Voltages and Currents:

1.) $A C$ voltages and the currents they produce are constantly varying not only in magnitude but also in direction. It would be nice to quantify such AC variables with some kind of timeINDEPENDENT, pseudo-average value ("pseudo" in the sense that a normal average will not work--the time-average of a sine wave is zero). As such, a bit of a different view has been taken. Consider the following:
a.) Hook a DC power source to a resistor (Figure 20.6a). The amount of power dissipated by the resistor will be $i^{2} R$, where $i$ is the constant DC current in the circuit.


FIGURE 20.6a
b.) Hook an AC power source to the
same resistor (Figure 20.6b). There will be power dissipated by the resistor, but how much? To determine this:
i.) At any instant $t$, the amount of power being dissipated by a resistor will be $i^{2} R$, where $i$ is the circuit's current at time $t$. Over an extended period of time, the power dissipated will be $\left(i^{2}\right)_{\text {avg }} R$. In other


FIGURE 20.6b words, the average of the current squared is the important quantity here.
ii.) AC current varies as shown in Figure 20.7. The AC current squared is also shown in that figure.
iii.) To a good approximation, the average of the AC current squared is $i_{0}^{2} / 2$. The power being dissipated by the resistor will, therefore, be:

$$
\begin{aligned}
\mathrm{P} & =\left(\mathrm{i}^{2}\right)_{\mathrm{avg}} \mathrm{R} \\
& =\left(\mathrm{i}_{0}^{2} / 2\right) \mathrm{R} .
\end{aligned}
$$



FIGURE 20.7
iv.) If we want to define a constant, time-independent current value-an effective current $i_{\text {eff }}$-that when squared and multiplied by $R$ will equal the amount of power dissipated by the resistor when in the AC circuit, we can write:

$$
\begin{aligned}
\left(\mathrm{i}_{\text {eff }}\right)^{2} \mathrm{R} & =\left(\mathrm{i}^{2}\right)_{\operatorname{avg}} \mathrm{R} \\
& =\left(\mathrm{i}_{0}^{2} / 2\right) \mathrm{R} .
\end{aligned}
$$

This implies that:

$$
\begin{aligned}
\mathrm{i}_{\text {eff }} & =\mathrm{i}_{0} /(2)^{1 / 2} \\
& =.707 \mathrm{i}_{0} .
\end{aligned}
$$

v.) Bottom line: The power-producing current provided to a circuit by an AC source has a DC equivalent. That DC equivalent (it was called the effective current above) is equal to $.707 \mathrm{i}_{\mathrm{o}}$, where $\mathrm{i}_{\mathrm{o}}$ is the amplitude of the AC current function.

Because this value was determined by taking the square root of the mean value of the square of the AC current, the value is called the "root mean square" value. In almost all physics textbooks, this is shortened to RMS. In other words, to be conventional, the effective current should be termed $\mathrm{i}_{\mathrm{RMS}}$.
vi.) Summary: The effective current in an AC circuit is called the RMS current and is equal to:

$$
\mathrm{i}_{\mathrm{rms}}=.707 \mathrm{i}_{\mathrm{o}}
$$

Similarly, the effective voltage in an AC circuit is called the RMS voltage and is equal to:

$$
V_{\mathrm{rms}}=.707 \mathrm{~V}_{\mathrm{o}} .
$$

c.) All AC meters read RMS values (both ammeters and voltmeters).
i.) Example: You are probably aware that most of the wall sockets in your home provide 110 volts AC at a frequency of 60 hertz (the few that do not are rated at 220 volts AC and are hooked to big appliances like washers and dryers). This means that if we plug the leads of an AC voltmeter into a wall-socket, the meter will read a constant RMS voltage of 110 volts.

With that in mind, write the function that characterizes a wallsocket's voltage as a function of time.

Solution: As stated, you know that the RMS voltage reading is 110 volts. The relationship between voltage amplitude $\mathrm{V}_{0}$ and RMS voltage $\mathrm{V}_{\text {RMS }}$ is:

$$
\begin{aligned}
& \mathrm{V}_{\mathrm{rms}}=.707 \mathrm{~V}_{\mathrm{o}} \\
& \Rightarrow \quad \mathrm{~V}_{\mathrm{o}}
\end{aligned}=\mathrm{V}_{\mathrm{rms}} / .707 .
$$

With $v=60 \mathrm{hz}$, the wall-socket voltage function is:

$$
\begin{aligned}
\mathrm{V}(\mathrm{t}) & =\mathrm{V}_{0} \sin (2 \pi v \mathrm{t}) \\
& =155.6 \sin (377 \mathrm{t}) .
\end{aligned}
$$

Note: Another voltage value sometimes cited is the peak-to-peak (more accurately, the peak-to-trough) voltage. By definition, $\mathrm{V}_{\mathrm{pp}}=2 \mathrm{~V}_{\mathrm{o}}$.

## D.) A Lone Resistor in an AC Circuit:

1.) We have already established that in DC circuits, the voltage across a resistor is directly proportional to the current through the resistor. Mathematically, this relationship is summed up in Ohm's Law which states that $V_{R}=i R$. The proportionality constant $R$ is a relative measure of the resistive nature of the circuit. That is, a large R implies a lot of resistance to charge flow whereas a small $R$ implies very little current resistance.


$$
V(t)=V_{0} \sin (2 \pi \vee t)
$$

FIGURE 20.8
2.) A resistor in an AC circuit (see Figure 20.8) behaves just as a resistor does in a DC circuit. At any instant, the voltage and current are related by Ohm's Law $\left(V_{r m s}=i_{r m s} R\right)$ and the power is related as $P=\left(i_{r m s}\right)^{2} R$.
3.) A power supply that can generate a variable frequency voltage (i.e., voltages at frequencies other than 60 hertz) is called a sine wave generator or a function generator.
4.) No matter what frequency a sine wave generator outputs into a resistor circuit, the resistive nature of the resistor will always be the same. Put another way, the resistive nature of a resistor is not frequencydependent.
5.) The current through a resistor and the voltage across a resistor will always be in phase. That is, as the voltage increases, so will the current. As the voltage decreases, so will the current. When the voltage is zero, so is the current, etc.

## E.) An Inductor and Resistor in an AC Circuit:

1.) Consider the RL circuit shown in Figure 20.9.
2.) The inductor in the circuit will have a certain amount of resistance $r_{L}$ inherent within the wires that make up its coils. That resistance will act like any other resistor-like element in an AC circuit.
3.) In addition to $r_{L}$, the inductor also has a resistive nature that is frequency-

RL, AC circuit


FIGURE 20.9 dependent. Not obvious? Follow along.
a.) When an alternating current passes through an inductor, Faraday's Law demands that an induced EMF be generated across the leads of the coil that will ultimately produce an induced magnetic flux that opposes the changing magnetic flux through the coil's face. From the previous chapter, the magnitude of this induced EMF equals:

$$
\varepsilon_{\mathrm{L}}=\mathrm{L}(\mathrm{di} / \mathrm{dt})
$$

b.) Writing a Loop Equation for an RL circuit (see Figure 20.10 for the voltages associated with each element), we get:

$$
\left(-\varepsilon_{\mathrm{L}}-\mathrm{ir}\right)-\mathrm{iR}+\mathrm{V}_{\mathrm{o}} \cos (2 \pi v \mathrm{t})=0
$$

In this expression, $\varepsilon_{\mathrm{L}}$ is the induced, fre-quency-dependent voltage drop across the in-


FIGURE 20.10 ductor (i.e., L di/dt), $2 \pi v$ is the angular frequency of the power supply, and a cosine function has been used to characterize the varying voltage across the power supply (I 've done this because it will make life easier when we do the evaluation that is to follow--we could as well have used a sine function, but the resulting expression would have been a bit messy). Substituting in and rearranging this expression, we get:

$$
\mathrm{L}(\mathrm{di} / \mathrm{dt})+\mathrm{ir}+\mathrm{iR}=\mathrm{V}_{\mathrm{o}} \cos (2 \pi \nu \mathrm{t}) .
$$

c.) The resistor-like resistance inherent in an inductor is sometimes negligibly small, sometimes not, so for the sake of simplicity we will lump it with $R$ to get $R_{\text {net }}$. Doing so, the above expression becomes:

$$
\mathrm{L}(\mathrm{di} / \mathrm{dt})+\mathrm{i} \mathrm{R}_{\mathrm{net}}=\mathrm{V}_{\mathrm{o}} \cos (2 \pi \nu \mathrm{t}) .
$$

d.) Though you will never have to derive this on a test, we need an expression for the resistive nature of the inductor excluding the resistor-like resistance inherent within its wires. To do this:
i.) Assume the resistance of (and, hence, voltage across) all of the resistor-like elements in the circuit is negligible (i.e., that $R_{\text {net }}$ $=0$ ). In that case, Kirchoff's Loop Equation becomes:

$$
\mathrm{L}(\mathrm{di} / \mathrm{dt})=\mathrm{V}_{\mathrm{o}} \cos (2 \pi v \mathrm{t})
$$

ii.) We know the voltage across the power supply and inductor. We'd like an expression for the current through the circuit. To determine this, we need to manipulate and integrate. Doing so yields:

$$
\begin{array}{rlrl}
\mathrm{L} \frac{\mathrm{di}}{\mathrm{dt}} & =\mathrm{V}_{\mathrm{o}} \cos (2 \pi v \mathrm{t}) \\
& \Rightarrow \quad \mathrm{Ldi}=\mathrm{V}_{0} \cos (2 \pi v \mathrm{t}) \mathrm{dt} \\
& \Rightarrow \quad \mathrm{~L} \int_{\mathrm{i}=0}^{\mathrm{i}} \mathrm{di}=\mathrm{V}_{0} \int_{\mathrm{t}=0}^{\mathrm{t}} \cos (2 \pi v \mathrm{t}) \mathrm{dt} \\
& \Rightarrow & \mathrm{Li}=\mathrm{V}_{\mathrm{o}}\left(\frac{1}{2 \pi v}\right)[\sin (2 \pi v \mathrm{t})] \\
& \Rightarrow & \left.\mathrm{i}=\frac{\mathrm{V}_{0}[\sin (2 \pi v \mathrm{t})]}{(2 \pi v \mathrm{~L})} \quad \text { (Equation } \mathrm{A}\right) .
\end{array}
$$

4.) Important point:
a.) Ohm's Law maintains that the current through an element must equal the voltage across the element divided by a quantity that reflects the resistive nature of the element. In the above expression, the voltage across the element is evidently $\mathrm{V}_{\mathrm{o}}[\sin (2 \pi \nu \mathrm{t})]$. That means the resistive nature of the inductor must be $2 \pi \nu \mathrm{~L}$.
b.) In fact, this is the frequency-dependent resistive nature of an inductor. It is called the inductive reactance, its symbol is $X_{L}$, and its units are ohms. Summarizing, we can write:

$$
X_{L}=2 \pi v \mathrm{~L} \quad \text { (ohms) },
$$

where the inductance $L$ must be written in terms of henrys (versus milli-henrys or whatever).
c.) Although we assumed the resistor-like resistance of the circuit was negligible to do the derivation, in fact this inductive reactance expression is true whether the resistor-like resistance is big or small.
5.) Side-N ote: Does the frequency-dependent expression for the resistive nature of an inductor (i.e., its inductive reactance) make sense? Consider:
a.) Consider a general RL circuit (i.e., one in which $\mathrm{R}_{\text {net }}$ is not small) hooked across a power supply that runs at low frequency.

Note: A low frequency voltage means that although the amplitude of the voltage of the power supply may be large or small, the rate at which the voltage changes is very slow.
i.) A low frequency voltage will produce a low frequency current.
ii.) A low frequency current means that di/dt will be small (the current is changing slowly if it is low frequency).
iii.) A small di/dt means the induced voltage drop across the inductor (Ldi/dt) is small.
iv.) A small induced voltage drop across the inductor implies a relatively large voltage drop across the resistor (at any instant, the two have to add up to the voltage across the power supply--a quantity that can be large).
v.) As the voltage drop across a resistor is directly proportional to the current through the resistor, a large voltage drop across the resistor implies a relatively large current through the resistor and, hence, through the circuit.
b.) Bottom line \#1: The current in an RL circuit will be relatively large when a low frequency signal passes through the circuit. That means we would expect the inductive reactance (the resistive nature of
the inductor) to be small at low frequencies. This is exactly what our derived expression predicts (i.e., when $v$ is small, $\mathrm{X}_{\mathrm{L}}=2 \pi \mathrm{~V}$ is small).
c.) Using similar reasoning, a power supply running at high voltage creates a high frequency current that will produce a very large di/dt value. In such a case, the voltage drop across the inductor (L di/dt) is relatively large and the voltage drop across the resistor is relatively small. A small voltage drop across the resistor suggests a small current flowing in the circuit.
d.) Bottom line \#2: The current in an RL circuit will be relatively small (i.e., approaching zero) when a high frequency signal passes through the circuit. That means we would expect the inductive reactance to be big at high frequencies. This is exactly what our expression predicts (i.e., when $v$ is large, $\mathrm{X}_{\mathrm{L}}$ is large).
e.) Summary: An inductor in an AC circuit passes low frequency signals while damping out high frequency signals. As such, inductors are sometimes referred to as low pass filters.
6.) The second point to note about Equation A again has to do with its form. By assuming a power supply voltage that is proportional to $\cos (2 \pi \nu t)$, and assuming that the net resistance in the circuit is zero (i.e., $R_{\text {net }}=0$ so that the voltage across the inductor is the same as that across the power supply), we find that the circuit's current is proportional to $\sin (2 \pi \nu t)$. Examining the graph of these two functions (the current is shown in Figure 20.11a and the voltage shown in Figure 20.11b) allows us to conclude that in this situation the voltage across the inductor leads the current through the inductor (i.e., the circuit's current) by $\pi / 2$ radians.

Note: This $\pi / 2$ phase shift exists ONLY if there is no resistor-like resistance in the circuit. As there will never be the case in which there is absolutely no resistor-like resistance in a circuit, the phase shift in a real AC circuit will never by $\pi / 2$. Calculating the real shift is something you will run into shortly.
current in an AC circuit

$\Delta \mathrm{i} / \Delta \mathrm{t}=$ "maximum;" This means the voltage ( $\mathrm{L} \Delta \mathrm{i} / \Delta \mathrm{t}$ ) is maximum at this point in time

FIGURE 20.11a
voltage across the inductor

the inductor's voltage LEADS the circuit's current by a quarter cycle

FIGURE 20.11b

## F.) A Capacitor and Resistor in an AC Circuit:

1.) Consider the RC circuit shown in Figure 20.12.
2.) Unless it is leaky, the capacitor in the circuit will have no resistor-like resistance inherent within it. As such, we will assume there is no ir voltage drop across the capacitor.
3.) J ust as there is a frequency-dependent resistive nature associated with an inductor, there is a frequency-dependent resistive nature associated with a capacitor. To see this, consider the following:
a.) The voltage drop across a

RC, AC circuit


FIGURE 20.12

$$
V_{C}=q / C
$$

where $q$ is the magnitude of charge on one capacitor plate and $C$ is the capacitor's capacitance.
b.) In this case, again to make the evaluation easier later on, let's assume the power supply's voltage is characterized as a sine function (a cosine function would also work, but it would be a little messy to make sense of later, so we'll use the sine). With that assumption, a Kirchoff's Loop Equation for this circuit (see Figure 20.13) becomes:

$$
-q / C-i R+V_{0} \sin (2 \pi v t)=0
$$

Manipulating, we get:


FIGURE 20.13
$\mathrm{V}(\mathrm{t})=\mathrm{V}_{\mathrm{o}} \sin (2 \pi \mathrm{~V} \mathrm{t})$

$$
q / C+i R=V_{0} \sin (2 \pi v t)
$$

where $q$ is a time varying quantity in the expression (we could denote it $\mathrm{q}(\mathrm{t})$, but, for simplicity, we will leave it as presented).
c.) Though you will never have to derive this on a test, we need an expression for the resistive nature of the capacitor. To do this:
i.) Assume the resistor-like resistance in the circuit is negligible (i.e., that R = 0). In that case, Kirchoff's Law becomes:

$$
\begin{aligned}
& q / C=V_{0} \sin (2 \pi v t) \\
& \Rightarrow q=C V_{0} \sin (2 \pi v t) .
\end{aligned}
$$

ii.) Remembering that $\mathrm{i}=\mathrm{dq} / \mathrm{dt}$, we can write:

$$
\begin{aligned}
\mathrm{i} & =\frac{\mathrm{dq}}{\mathrm{dt}} \\
& =\frac{\mathrm{d}\left[C V_{0} \sin (2 \pi v t)\right]}{\mathrm{dt}} \\
& =C V_{0}(2 \pi v) \cos (2 \pi v t)
\end{aligned}
$$

$$
=\frac{V_{0} \cos (2 \pi \nu t)}{\left(\frac{1}{2 \pi \nu C}\right)} \quad \text { (Equation } B \text { ). }
$$

4.) As pointed out in the inductor section, Ohm's Law maintains that the current through an element must equal the voltage across the element divided by a quantity that reflects the resistive nature of the element. In the above expression, the voltage across the element is $\mathrm{V}_{0} \cos (2 \pi \nu \mathrm{t})$. That means the resistive nature of the capacitor must be $1 /(2 \pi \nu \mathrm{C})$.

In fact, this is the frequency-dependent resistive nature of a capacitor. It is called the capacitive reactance, its symbol is $X_{C}$, and its units are ohms. Summarizing, we can write:

$$
X_{C}=\frac{1}{2 \pi v C} \quad \text { (ohms), }
$$

where the capacitance C must be written in terms of farads (versus leaving it in microfarads or whatever).
5.) Side-N ote: Does the frequency-dependent expression for the resistive nature of a capacitor (i.e., its capacitive reactance) make sense? Consider:
a.) Assume the voltage of a power supply runs at low frequency.
i.) Examining the low frequency signal shown in Figure 20.14a, it is evident that the signal is changing very slowly and that there is a respectable amount of charge on the capacitor a fair portion of the time. In other words, the capacitor has plenty of time to charge up and, on the average, the voltage ( $q / C$ ) across the capacitor is relatively large.

Iow frequency voltage


FIGURE 20.14a
ii.) Because the capacitor's
voltage is relatively large on average, the voltage across the resistor will be relatively small. This implies a small current in the circuit.
b.) Bottom line \#1: The current in an RC circuit will be relatively small (i.e., zero) when a low frequency signal passes through the circuit. That means we would expect the capacitive reactance (the resistive nature of the capacitor) to be large at low frequencies. This is exactly what our expression predicts (i.e., when $v$ is small, $X_{C}=1 /(2 \pi \nu C)$ is large).
c.) Assume the voltage of a power supply now runs at high frequency.
i.) Examining the high frequency signal shown in Figure 20.14b, it is evident that the signal is changing very fast. There are not great spans of time during which the capacitor is charged, hence there are not great spans of time during which the voltage across the capaci-
high frequency voltage

the current charges and discharges the cap so fast that the capacitor's voltage, on average, goes to zero tor is high. In fact, the voltage (on the average) is low FIGURE 20.14b (remember, the time average of a high frequency sine wave is zero even over relatively small time intervals).
ii.) A small voltage across the capacitor (on average) means a large voltage across the resistor. This implies a large current in the circuit.
d.) Bottom line \#2: The current in an RC circuit will be relatively large when a high frequency signal passes through the circuit. That means we would expect the capacitive reactance to be small at high frequencies. This is exactly what our expression predicts (i.e., when $v$ is large, $X_{C}$ is small).
e.) Summary: A capacitor in an AC circuit passes high frequency signals while damping out low frequency signals. As such, capacitors are sometimes referred to as high pass filters.
6.) The second point to note about Equation B is, again, its form. By assuming a power supply voltage that is proportional to $\sin (2 \pi \nu t)$, and assuming that the net resistance in the circuit is zero (i.e., $\mathrm{R}_{\text {net }}=0$ so that the voltage across the inductor is the same as that across the power supply), we find that the circuit's current is proportional to $\cos (2 \pi \nu t)$. Examining the graph of these two functions allows us to conclude that in this situation the voltage across the capacitor lags the current through the capacitor (i.e., the circuit's current) by $\pi / 2$ radians.

Does this make sense?
a.) The voltage across a capacitor is proportional to the charge on the capacitor (i.e., $\mathrm{V}_{\mathrm{C}}$ $=q / C$ ). Figure 20.15a depicts a graphical representation of this.
b.) Current is defined as the amount of charge that passes a particular point per unit time (i.e., $\mathrm{i}=\mathrm{dq} / \mathrm{dt}$ ).
c.) The slope of the capacitor's voltage function is
voltage across a capacitor in an AC circuit

$\Delta \mathrm{V} / \Delta \mathrm{t}=$ "maximum;" This means the current $(\Delta q / \Delta t)$ is maximum at this point in time

FIGURE 20.15a

the capacitor's voltage LAGS
the circuit's current by a quarter cycle
fied current function. Figure 20.15b shows this.
e.) In comparing the graphs, it is evident that the voltage across the capacitor LAGS the current in the circuit by one quarter of a cycle, or $\pi / 2$ radians.

N ote: As was the case with the inductor, this $\pi / 2$ phase shift exists ONLY if there is no resistor-like resistance in the circuit. As there will never be the case in which there is absolutely no resistor-like resistance in a circuit, the phase shift in a real RC circuit will never by $\pi / 2$. Calculating what it actually is in a given case is something you will run into shortly.

## G.) A Capacitor, Inductor, and Resistor in an AC Circuit:

1.) Until now, the algebraic expressions defining the resistive natures of capacitors and inductors have been perfectly straightforward. However, something a little more complex happens when a capacitor, inductor, and resistor are all placed in the same AC circuit (see Figure 20.16). As capacitors dampen out low frequency while inductors dampen out high frequency, the question arises, "Is there any frequency at which a current can flow?"

It turns out that there is one frequency (or a small range of frequencies) where the effect of the
an RLC, AC circuit


FIGURE 20.16 capacitor is negated by the effect of the inductor. At that frequency, the circuit's net resistive nature becomes small and a relatively large current flows. To determine that frequency, we must first determine an expression for the net resistive nature of the RLC circuit. To do that, we could do as we did with the RC and RL circuits. That is, we could write out Kirchoff's Loop equation for the circuit, solve for the current, then compare that expression with the Ohm's Law relationship $\mathrm{i}=\mathrm{V} /($ resistive nature) to determine the circuit's net resistive nature.

As mathematically intriguing as this might be, there is another way utilizing what are called phasor diagrams.
2.) Phasor diagrams:
a.) For a given frequency, there are three vectors in a phasor diagram:
i.) The magnitude of the first vector is equal to the magnitude of the net resistive nature provided by the elements in the circuit
that do not throw the voltage out of phase with the current at all. These elements are resistors. Their net resistive nature is denoted by $R_{\text {net }}$ (in most cases, this $R_{\text {net }}=R+r_{L}--$ remember, there is usually resistor-like resistance inherent in the wire making up the inductor coil). This vector will be graphed al ong the $+x$ axis.

Note: In this kind of diagram, circuit elements that leave the voltage in phase with the current are graphed BY DEFINITION along the +x axis. Elements that make the voltage lead the current by a quarter cycle are graphed at $+\pi / 2$ radians from the $+x$ axis (i.e., along the $+y$ axis), and elements that make the voltage lag the current by a quarter cycle are graphed at $-\pi / 2$ radians (i.e., al ong the $-y$ axis).
ii.) The magnitude of the second vector is equal to the magnitude of the net resistive nature provided by the elements in the circuit that make the voltage lead the current by $\pi / 2$ radians. These elements are the inductors. Their net resistive nature is denoted by $X_{L}$ and that quantity is frequency-dependent. This vector will be graphed along the +y axis.
iii.) The magnitude of the third vector is equal to the magnitude of the net resistive nature provided by the elements in the circuit that make the voltage lag the current by $\pi / 2$ radians. These ele ments are capacitors.
Their net resistive nature is denoted by $\mathrm{X}_{\mathrm{C}}$ and that quantity is frequency-dependent. This vector will be graphed along the -y axis.
b.) A typical phasor diagram is shown in


FIGURE 20.17a
Figure 20.17a.
The vector
addition of the two vectors found along the $y$ axis is shown in Figure 20.17b, and the final vector addition (i.e., the one that produces the resultant $Z$ ) is shown in Figure 20.17c.
3.) Impedance:
a.) The resultant of the phasor diagram is given a special name--the circuit's impedance (its symbol is Z). It tells us two things: the net resistive nature of the entire circuit and the phase shift of the circuit (i.e., the degree to which the voltage leads or lags the current at a given frequency).
b.) Algebraically (using the Pythagorean relationship and the diagram), the magnitude of a circuit's impedance equals:
vector addition in the " $y$ " direction


FIGURE 20.17b

IMPEDANCE: the vector addition of all of the elements


FIGURE 20.17c

$$
\mathrm{Z}=\left[\mathrm{R}_{\mathrm{net}}^{2}+\left(\mathrm{X}_{\mathrm{L}}-\mathrm{X}_{\mathrm{C}}\right)^{2}\right]^{1 / 2}
$$

c.) The units of $Z$ are ohms.
d.) Writing $Z$ out in expanded form, we find that it is frequencydependent:
$Z=\left[R_{\text {net }}{ }^{2}+[2 \pi v L-1 /(2 \pi \nu C)]^{2}\right]^{1 / 2}$.
e.) Using trig. and the phasor


FIGURE 20.17d diagram shown in Figure 20.17d, the phase shift is found to be:

$$
\phi=\tan ^{-1}\left[\left(X_{\mathrm{L}}-\mathrm{X}_{\mathrm{C}}\right) / \mathrm{R}_{\mathrm{net}}\right]
$$

Note: If the calculated phase shift is positive, the voltage leads the current. If the phase shift is negative, the voltage lags the current.
4.) The Resonance Frequency of an RLC circuit: We began this section on RLC circuits by posing a question: "Is there a frequency at which the resistive effects of the inductor and capacitor cancel one another out leaving us with a substantial current flowing in the circuit?"

Our impedance expression gives us the answer.
a.) As both $X_{L}$ and $X_{C}$ are frequency-dependent, there must be a frequency at which $X_{L}-X_{C}$ equals zero. At that frequency, the net resistive quality of the circuit (i.e., its impedance) will be at a minimum, and a relatively sizable current should flow.

This special frequency is called the resonance frequency $v_{\text {res }}$.
b.) Mathematically, the resonance frequency can be found as follows: At resonance,

$$
\begin{gathered}
\mathrm{X}_{\mathrm{L}}-\mathrm{X}_{\mathrm{C}}=0 \\
\Rightarrow \quad 2 \pi v_{\text {res }} \mathrm{L}-1 /\left(2 \pi v_{\mathrm{res}} \mathrm{C}\right)=0 \\
\Rightarrow v_{\text {res }}=\frac{1}{2 \pi} \sqrt{\frac{1}{\mathrm{LC}}}
\end{gathered}
$$

A graph of the frequency response (i.e., current as a function of frequency) of an RLC circuit is shown in Figure 20.18.
5.) One might wonder what is really happening at resonance. A more concise explanation follows:
a.) Assume a capacitor in an LC circuit is initially charged (see Figure 20.19a--there is resistance in the circuit but we will ignore it for now). There is no power supply in the circuit, so when the switch is thrown at $t=0$ the capacitor begins to discharge through the inductor.
b.) As a coil, the inductor produces a back-EMF which
frequency response of an RLC circuit


FIGURE 20.18


FIGURE 20.19a
fights the increase of current in the circuit. As such, the current rises more slowly than would otherwise have been the case (though it does still rise).
c.) Sooner or later, the free charge on the discharging capacitor begins to run out. As this occurs, the current in the circuit declines prompting the inductor to produce an induced EMF that fights that change.
d.) Bottom line: Charge continues to flow in the circuit even after the capacitor is completely discharged.
e.) As current continues to flow, the capacitor begins to recharge going the other way (i.e., what was the positive terminal becomes the negative terminal and vice versa). When the induced current finally dies out, we find ourselves with a charged capacitor all ready to discharge, starting the whole process over again.
f.) If there is no resistance in the circuit (i.e., if the system's wires are superconductors), this oscillating AC charge flow will continue forever. With resistance in the circuit, the amplitude of the current will diminish with time but the charge/discharge frequency will not change (see Figure 20.19b).
g.) The natural frequency of this
damped current in an oscillating RLC circuit w/o power supply


FIGURE 20.19b discharge, charge, discharge process is related to the size of the inductor and capacitor. Specifically, the system's natural frequency is $\frac{1}{2 \pi} \sqrt{\frac{1}{\mathrm{LC}}}$.
h.) It shouldn't be hard to imagine what will happen if a power supply is placed in a circuit. If the frequency of the power supply does not match the natural frequency of the (R)LC combination, the charge/discharge frequency will fight the alternative voltage provided by the out-of-step power supply, and the net current in the circuit will be very small if not zero.

On the other hand, if the frequency of the power supply is just right (i.e., $\frac{1}{2 \pi} \sqrt{\frac{1}{\mathrm{LC}}}$ ), its voltage will resonate with the charge/discharge cycle and the amplitude of the net current in the circuit will grow large. That frequency is the resonance frequency for the (R)LC circuit, and at that frequency the current will be as large as it ever can be.

## H.) Summary of Circuit Elements in AC Circuits:

1.) The following is a chart devoted to presenting the information outlined above in an easily digested format. KNOW WHAT THE SYMBOLS MEAN--simply memorizing relationships is NOT going to help you on your next test!

| element | symbl | units | resistive nature | phase | filter |
| :---: | :---: | :---: | :---: | :---: | :---: |
| resistor | R | ohms | resistance R (ohms) | no phase shift | none |
| inductor | L | henrys | inductive reactance: $X_{L}=2 \pi v L$ <br> (ohms) | $\mathrm{V}_{\mathrm{L}}$ leads circuit current by $\pi / 2$ radians | low pass |
| capacitor | C | farads | capacitive reactance: $X_{C}=1 /(2 \pi v C)$ <br> (ohms) | $\mathrm{V}_{\mathrm{C}}$ lags circuit current by $\pi / 2$ radians | high pass |
| RLC circuit | $-$ | ---- | impedance: $\mathrm{Z}=\left[\mathrm{R}^{2}+\left(\mathrm{X}_{\mathrm{L}}-\mathrm{X}_{\mathrm{C}}\right)^{2}\right]^{1 / 2}$ <br> (ohms) | $\begin{gathered} \text { phase shift }(\phi) \\ =\tan ^{-1}\left[\left(X_{L}-X_{C}\right) / R\right] \\ \text { (radians) } \end{gathered}$ | re-sonance frequ. |

Note: Resonance frequency is $v_{\text {res }}=\frac{1}{2 \pi} \sqrt{\frac{1}{\mathrm{LC}}}$ hertz.

## I.) Impedance Matching and Transformers:

1.) Optical light passing through an interface (i.e., a boundary) between two media will normally experience partial reflection caused by the fact that the two media have different densities. The only time a light beam will not reflect is when it passes from one medium into a second medium whose "density environment" is exactly the same as the first.
2.) Consider any complex electrical circuit--say, a stereo system connected to speakers. If the impedance of the stereo and the impedance of the collective speakers is the same, the signal will pass from the one to the other just as light passes through two common-density environments (i.e., there will be no reflection at the interface between the two systems). If, on the other hand, the resistive nature of the circuitry from which the signal comes (i.e., the stereo) is different from the resistive nature into which the signal must go (i.e., the speakers), reflection will occur at the interface. Put
another way, maximum power will be transferred from the stereo to the speakers when the impedance of both is the same.
3.) The problem frequently confronting circuit designers is the fact that stereo systems have large impedances whereas speaker circuits have only tiny impedances. The question is, "How does one trick the signal into thinking the circuit it is entering has the same impedance as the circuit it is leaving?"

The answer involves the use of a transformer and is wrapped up in what is called impedance matching.
4.) A quick review of transformers: A transformer is essentially a pair of coils linked via a common magnetic field and, hence, a common magnetic flux. The turns-ratio ( $\mathrm{N}_{\mathrm{s}} / \mathrm{N}_{\mathrm{p}}$ ) dictates how the secondary's voltage and current is related to the primary's voltage and current. That is:
a.) $N_{s} / N_{p}=\varepsilon_{s} / \varepsilon_{p}=i_{p} / i_{s}$ (this is true in all cases). Additionally;
b.) If $\mathrm{N}_{\mathrm{s}}<\mathrm{N}_{\mathrm{p}}$, the secondary's voltage is smaller than the primary's voltage ( $\varepsilon_{\mathrm{s}}<\varepsilon_{\mathrm{p}}$ ) and the transformer is called a step-down transformer. In step-down transformers, the current in the secondary is larger than the current in the primary (i.e., $\mathrm{i}_{\mathrm{s}}>\mathrm{i}_{\mathrm{p}}$ ).
c.) If $N_{s}>N_{p}$, the secondary's voltage is larger than the primary's voltage ( $\varepsilon_{\mathrm{s}}>\varepsilon_{\mathrm{p}}$ ) and the transformer is called a step-up transformer. In step-up transformers, the current in the secondary is smaller than the current in the primary (i.e., $i_{s}<i_{p}$ ).
5.) Having so reviewed, consider the following situation. A $1200 \Omega$ stereo system $\left(Z_{\text {st }}=1200 \Omega\right)$ is hooked up to a set of $8 \Omega$ speakers ( $\mathrm{Z}_{\mathrm{sp}}=8 \Omega$ ). From the signal's standpoint, how can we use a transformer to make $8 \Omega$ speakers look like $1200 \Omega$ elements?
a.) The first thing to notice is that as the signal comes in to the transformer, it sees a net impedance ( $Z_{\text {transf. Hoad }}$ ) made up of the primary coil's impedance, the secondary coil's impedance, and the load's impedance $\left(Z_{\text {load }}\right)$. This net impedance is what we want to numerically equal the stereo's impedance ( $Z_{\mathrm{st}}$ ). Put another way, the signal sees an entire package which, if the transformer system has been designed optimally, will appear to have an impedance of $1200 \Omega$.
b.) We know that the primary coil's current $i_{p}$ will be the current coming from the stereo (i.e., $i_{p}=i_{\text {stereo }}$ ) while the primary coil's voltage is some value $\mathrm{V}_{\mathrm{p}}$. From Ohm's Law, the impedance of the stereo circuit $\left(Z_{\text {st }}\right)$ will be:

$$
Z_{s t}=V_{p} / i_{p} \quad(=1200 \Omega \text { for our example }) .
$$

c.) As the current from the stereo is $i_{p}$ and the impedance of the stereo is $\mathrm{Z}_{\mathrm{st}}$ the energy provided by the stereo to the primary coil will be:

$$
P_{p}=i_{p}^{2} z_{s t}
$$

d.) Assuming an ideal transformer, the power provided by the primary will be completely transferred to the secondary. That is:

$$
P_{p}=P_{s}
$$

e.) We know that the energy provided to the secondary circuit (i.e., the power available to the secondary circuit from the primary coil) will be dissipated by the load (the speakers). That is:

$$
P_{s}=i_{s}{ }^{2} Z_{\text {load }}
$$

f.) Equating the power terms yields:

$$
\begin{gathered}
P_{p}=P_{s} \\
i_{p}^{2} z_{s t}=i_{s}^{2} Z_{\text {load }} \\
\Rightarrow \quad z_{s t}=\left(i_{s}{ }^{2} / i_{p}^{2}\right) z_{\text {load }}
\end{gathered}
$$

(Equation A).
g.) We have already established the relationships that exist between the secondary and primary currents and the turns-ratio of the transformer. Specifically, we know that:

$$
N_{p} / N_{s}=i_{s} i_{p}
$$

h.) Using this to eliminate the current terms in Equation $A$ leaves us with:

$$
\mathrm{Z}_{\mathrm{st}}=\left(\mathrm{N}_{\mathrm{p}}{ }^{2} / \mathrm{N}_{\mathrm{s}}{ }^{2}\right) \mathrm{Z}_{\text {load }} .
$$

i.) What does this relationship mean? $Z_{\text {st }}$ and $Z_{\text {load }}$ are fixed. Evidently, for the signal to transfer without reflection, the turns-ratio of the transformer must be such that:

$$
\left(N_{p} / N_{s}\right)^{2}=Z_{s t} / Z_{\text {load }}
$$

j.) Bottom line: To modify the speaker-load to suit the incoming signal (i.e., to impedance match), we must use a transformer whose turns-ratio is such that:

$$
\left(N_{p} / N_{s}\right)^{2}=Z_{s t} / Z_{\text {load }}
$$

where $Z_{\text {load }}$ is the true load resistance (i.e., that of the speakers) and $Z_{s t}$ is the impedance of the signal's source.
k.) F or our situation, $\mathrm{N}_{\mathrm{p}} / \mathrm{N}_{\mathrm{s}}=(1200 \Omega / 8 \Omega)^{1 / 2}=12.24 / 1$. If the winds are $N_{p}=1224$ and $N_{s}=100$, the signal will see the load as $1200 \Omega$, and no reflection will occur.

## QUESTIONS

20.1) An AC voltage source is found to produce a 12 volt peak to peak signal at 2500 hertz.
a.) Characterize this voltage as a sine function.
b.) Determine the RMS voltage of the source.
c.) It is found that when a capacitor and resistor are placed across the source as characterized above, an ammeter in the circuit reads 1.2 amps. What is the maximum current drawn from the source?
20.2) An RC circuit is hooked across an AC power supply. Which of the following statements are true (there can be more than one)? Explain each response.
a.) The RMS voltage across the resistor is the same as the average voltage across the resistor.
b.) The RMS voltage across the resistor is equal to R times the RMS current through the resistor.
c.) The RMS voltage across the resistor will be very large if the capacitive reactance is very large.
d.) The RMS current in the circuit will be very large if the capacitive reactance is very small.
e.) A decrease in frequency will increase the voltage across the capacitor.
f.) An increase in the capacitance will increase the current in the circuit for a given frequency.
g.) A decrease in frequency will increase the voltage across the resistor.
20.3) An RL circuit is hooked across an AC power supply. Assuming the inductor's internal resistance $r_{L}$ equals zero, which of the following statements are true (there can be more than one)?
a.) The RMS voltage across the resistor is equal to R times the RMS current through the resistor.
b.) The RMS voltage across the resistor R will be very large if the inductive reactance is very large.
c.) The RMS current in the circuit will be very large if the inductive reactance is very small.
d.) A decrease in frequency will increase the voltage across the inductor.
e.) An increase in the inductance will increase the current in the circuit for a given frequency.
f.) A decrease in frequency will increase the voltage across the resistor.
20.4) Considering the circuit shown in Figure I, which of the resistors will dissipate most of the power (that is, energy) at a very high frequency?


FIGURE I
20.5) A circuit has 220 ohms of impedance when a 95 ohm resistor is connected in series with a capacitor and a power source ( $\mathrm{V}_{\text {max }}=80$ volts) supplying power at 300 hertz.
a.) What is the capacitive reactance at this frequency?
b.) Determine the circuit's impedance at 1000 hertz.
c.) Determine the circuit's RMS current at 1000 hertz.
20.6) A circuit driven at 240 hertz has 60 ohms of impedance when a 12 ohm resistor is connected in series with an inductor whose internal resistance is 8 ohms. The circuit's power source provides 70 volts RMS.
a.) What is the inductive reactance at this frequency?
b.) Determine the circuit's impedance at 1000 hertz.
c.) Determine the circuit's RMS current at 1000 hertz.
20.7) An RLC circuit incorporates a 12 ohm resistor, a 60 mH inductor, and a $12 \mu \mathrm{f}$ capacitor hooked in series across a power supply whose timedependent voltage is $140 \sin (1100 \mathrm{t}$ ).
a.) Determine the frequency of the power supply's signal.
b.) Determine the capacitive reactance at this frequency.
c.) Determine the inductive reactance at this frequency.
d.) Determine the impedance of the circuit at this frequency.
e.) Determine the phase shift at this frequency. Is the voltage leading or lagging the current?
f.) Determine the power supply's RMS voltage.
g.) Determine the RMS current in the circuit at this frequency.
h.) Determine the resonance frequency for this circuit.
i.) Write out the time-dependent power supply voltage function at resonance.
j.) Determine the impedance of the circuit at resonance.
k.) Determine the RMS current in the circuit at resonance.
20.8) A transformer has 200 turns in its primary coil. A radio circuit has 237 ohms of impedance. Its signal has to be played through a 12 ohm speaker.
a.) Impedance match the two circuits. That is, explain how you would effect the match and give any pertinent numbers.
b.) Did you use a step-down or step-up transformer?

